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Date of Deposit: 5/17/02
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Signature: Karen Cing-Mann 5/17/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
In Re Application of Bolam et al.	May 17, 2002
	Examiner: Fetsum Abraham
Serial No: 09/878,681 Filing Date: June 11, 2001	Group Art Unit 2826
Title: SILICON-ON-INSULATOR CHIP HAVING AN ISOLATION BARRIER FOR RELIABILITY	IBM Corporation 2070 Route 52 Dept. 18G, Bldg. 300-482 Hopewell Junction New York 12533

AC
Response
w/
Terminal
Disclaimer

RESPONSE

The Commissioner for Patents
Washington, D.C. 20231

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TECHNOLOGY CENTER 2800

Sir:

In response to the Office Action dated 3/05/02, applicant replies as follows:

REMARKS

Applicant submits a Terminal Disclaimer referencing commonly-owned U.S. Patent No.